

# VRT100A

Rear I/O for VadaTech VPX100  
GPGPU, VPX RTM

VRT100A



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## Key Features

- 3U Rear Transition Module (RTM) per VITA 46
- Display Port break out for VadaTech VPX100 GPGPU

## Benefits

- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company

**OpenVPX™**



# VRT100A

The VRT100A is a 3U VPX Rear Transition Module providing I/O expansion for use with the VadaTech VPX100 GPGPU.

Quad Display Port are routed to the rear transition module (RTM) from the front module (VadaTech VPX100 GPGPU).

*Figure 1: VRT100A*

## Block Diagram

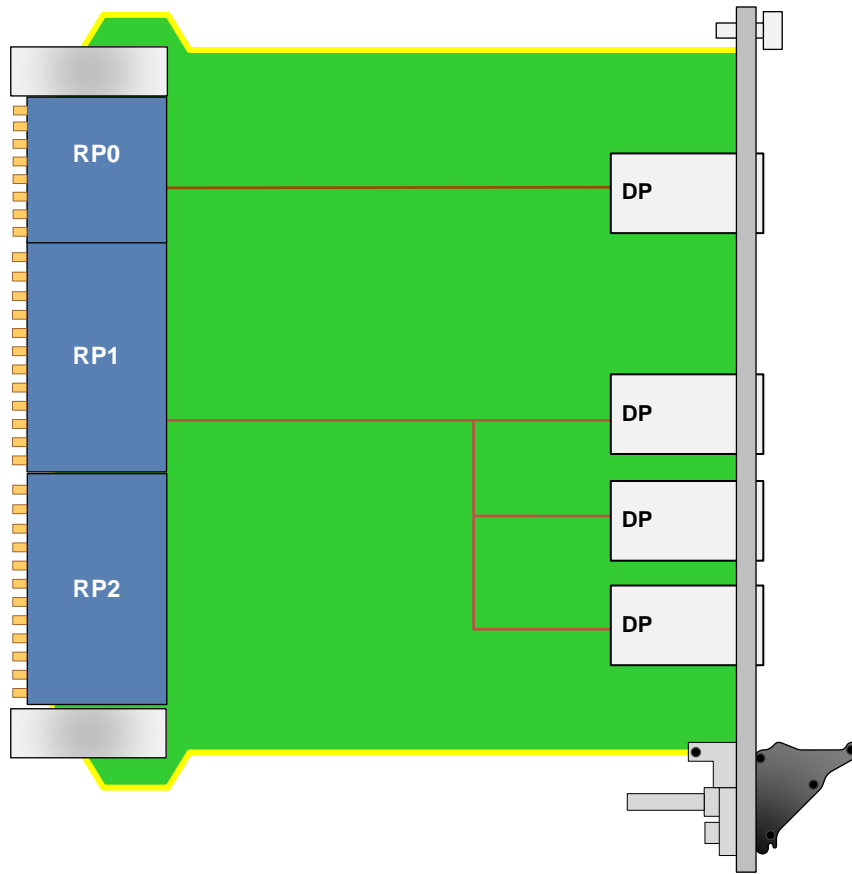


Figure 2: VRT100A Functional Block Diagram

## Back Panel

Figure 3: VRT100A Back Panel

# Specifications

Architecture		
Physical	Dimensions	3U RTM, 1" pitch
Configuration		
Power	VRT100A	0.5W
Rear Panel	Connectors	Quad DP
VPX Interfaces	Slot Profiles	See <a href="#">Ordering Options</a>
	Backplane	RP1/RP2
	Power Supplies	RP0: VS1 = 12V
Other		
MTBF	MIL Hand book 217-F@ TBD hrs	
Certifications	Designed to meet FCC, CE and UL certifications, where applicable	
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards	
Warranty	Two (2) years, see <a href="#">VadaTech Terms and Conditions</a>	

## INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

# Ordering Options

## VRT100A – 000-000-GHJ

		<b>G = Applicable Slot Profiles</b>
		0 = 5 HP
		<b>H = Environmental</b>
		See <a href="#">Environmental Specification</a>
		<b>J = Conformal Coating</b>
		0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

## Environmental Specification

Option H	Air Cooled			Conduction Cooled	
	H = 0	H = 1	H = 2	H = 3	H = 4
<b>Operating Temperature</b>	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
<b>Storage Temperature</b>	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
<b>Operating Vibration</b>	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
<b>Storage Vibration</b>	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
<b>Humidity</b>	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes: \*Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

## Related Products

VPX518



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package (XC7Z100 or XC7Z045)
- Protocols such as PCIe, SRIO, 10GbE/40Gbe, etc. are FPGA programmable

VPX754



- 3U VPX module Intel 5th Generation Xeon D-1577, D-1548 or D-1520 (Broadwell) System-on-Chip (SoC)
- PCIe Gen3 dual x4 or single x8
- Front-panel video out via micro HDMI

VTX870



- Open VPX benchtop development platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots

# Contact

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## We are technology leaders

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- Constant innovation
- Open systems expertise

## We commit to our customers

- Partnerships power innovation
- Collaborative approach
- Mutual success

## We deliver complexity

- Complete signal chain
- System management
- Configurable solutions

## We manufacture in-house

- Agile production
- Accelerated deployment
- AS9100 accredited



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