

VRT584A

Rear I/O for VPX584, VPX RTM



VRT584A

Key Features

- 3U RTM per VITA 46
- Dual QSFP28 (100GbE and/or 40GbE or quad 10GbE)
- Single GbE
- GPIO

Benefits

- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company

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VRT584A

The VRT584A is a 3U VPX Rear Transition Module providing I/O expansion for use with the VPX584.

Dual QSFP28 are routed from RP1 to back panel with an additional GbE via RJ-45. The module has a CDR on board per each QSFP28.

The module also has a 0.1" header for user defined I/O which is configurable by the FPGA as seven LVDS or 14 Single Ended.



Figure 1: VRT584A

Block Diagram

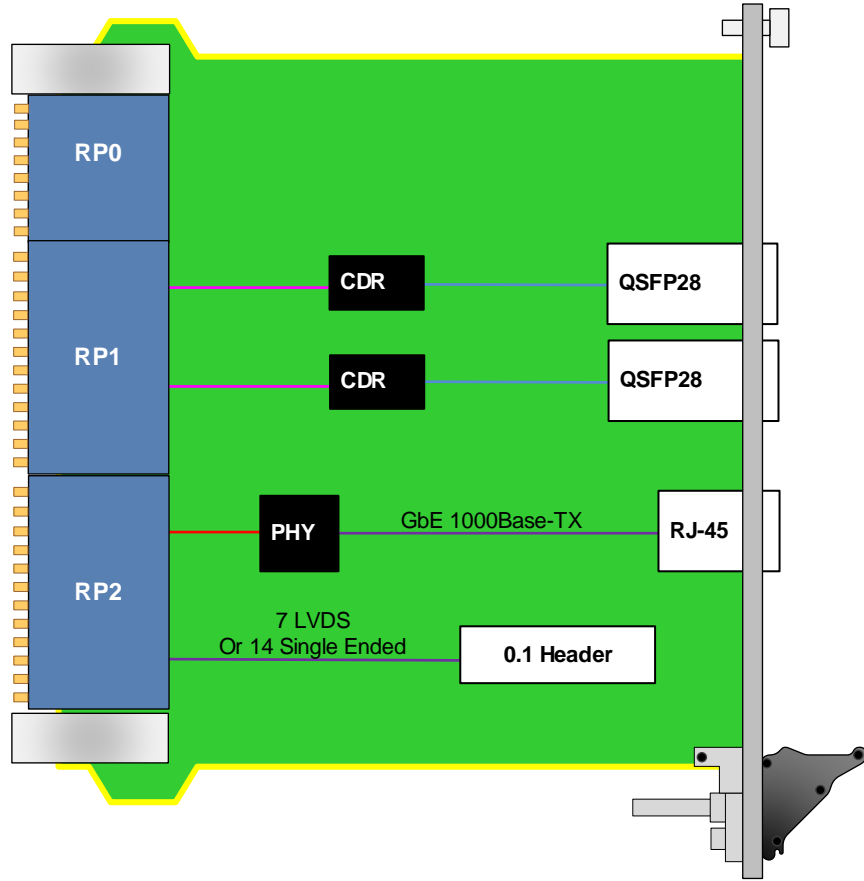


Figure 2: VRT584A Functional Block Diagram



Figure 3: VRT584A Front Panel View

Specifications

Architecture		
Physical	Dimensions	3U RTM, 1" pitch
Configuration		
Power	VRT584A	6W
Rear Panel	Connectors	Dual QSFP28 Single GbE via RJ-45 GPIO to 0.1" Header
VPX Interfaces	Slot Profiles	See Ordering Options
	Backplane	RP0: Power RP1: Dual high speed SERDES to QSFP28 ports RP2: Single GbE RP2: I/O
	Power Supplies	RP0: VS2 +3.3V and VS3 +5V
Other		
MTBF	MIL Hand book 217-F@ TBD hrs	
Certifications	Designed to meet FCC, CE and UL certifications, where applicable	
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards	
Warranty	Two (2) years, see VadaTech Terms and Conditions	

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

Ordering Options

VRT584A – AB0-000-GHJ

A = 100G/40G Transceivers* 0 = None 1 = 100G-SR 2 = 100G-LR (1KM) 3 = 100G-ER (10KM) 4 = 40G-SR 5 = 40G-LR (1KM) 6 = 40G-ER (10KM)		G = Applicable Slot Profiles 0 = 5 HP
B = Optical cable interface 0 = None (for option A = 0) 1 = LC style 2 = MTP/MPO		H = Environmental See Environmental Specification
		J = Conformal Coating 0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

*Dual Transceivers are shipped

Environmental Specification

Option H	Air Cooled			Conduction Cooled	
	H = 0	H = 1	H = 2	H = 3	H = 4
Operating Temperature	AC1* (0°C to +55°C)	AC3* (-40°C to +70°C)	CC1* (0°C to +55°C)	CC3* (-40°C to +70°C)	CC4* (-40°C to +85°C)
Storage Temperature	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C1* (-40°C to +85°C)	C3* (-50°C to +100°C)	C3* (-50°C to +100°C)
Operating Vibration	V2* (0.04 g2/Hz max)	V2* (0.04 g2/Hz max)	V3* (0.1 g2/Hz max)	V3* (0.1 g2/Hz max)	V3 (0.1 g2/Hz max)
Storage Vibration	OS1* (20g)	OS1* (20g)	OS2* (40g)	OS2* (40g)	OS2* (40g)
Humidity	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing	95% non-condensing

Notes: *Nomenclature per ANSI/VITA 47. Contact local sales office for conduction cooled (H = 2, 3, 4)

Related Products

VPX584



- 3U FPGA carrier for FPGA Mezzanine Card (FMC) per VITA 46 and VITA 57
- Xilinx Zynq-7000 FPGA in FFG-900 package (XC7Z100 or XC7Z045)
- Protocols such as PCIe, SRIO, 10GbE/40GbE, etc. are FPGA programmable

VPX754



- 3U VPX module Intel 5th Generation Xeon D-1577, D-1548 or D-1520 (Broadwell) System-on-Chip (SoC)
- PCIe Gen3 dual x4 or single x8
- Front-panel video out via micro HDMI

VTX870



- Open VPX benchtop development platform
- Dedicated Switch/management slot
- Up to five 3U VPX payload slots

Contact

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