VT007

ATCA and/or VPX Bench-Top Shelf Manager and/or Protocol Analyzer



Key Features

- Bench-top stand-alone Shelf Manager
- IPMI protocol analyzer that interfaces to any ATCA and/or VPX chassis via cable
- 3W max power, 12V input
- Quad Core ARM @ 1 GHz per core
- One GB of DDR3 Memory
- Dual 10/100 Ethernet Ports
- RS-232 Debug Port
- Field upgradable with dual boot flash
- Telco alarms

Benefits

- Rich set of management software (refer to the VT001 specification for all software components) such as HPI, RMCP, SNMP, CLI, HTTP, etc.
- Electrical, mechanical, software, and system-level
 expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company





THE POWER OF VISIO

VT007

The VadaTech VT007 Shelf Manger is a bench-top, stand-alone shelf manager and can manage any number of ATCA and/or VPX modules during debugging and development. The unit can be interfaced to any ATCA and/or VPX chassis via cable to run as a protocol analyzer to monitor, inject, capture and validate I2C traffic on the Intelligent Platform Management Bus (IPMB).

A Graphical User Interface (GUI) validates and displays the IPMI packets or schedules IPMI messages for injection into the shelf. The GUI application communicates with the VT007 through the Ethernet Port.

A combination of the VT000 and the VT007 can manage any number of ATCA modules during development and debugging. The module can also be used in VadaTech VPX Chassis such as VTX980/981/982/990/991 etc.

The VT007 utilizes the common VadaTech VT003 module as its shelf manager or protocol analyzer.

VadaTech can modify this product to meet special customer requirements without NRE (minimum order placement is required).



Figure 1: VT007 Front View



Figure 2 VT007 Rear View

2

Block Diagram

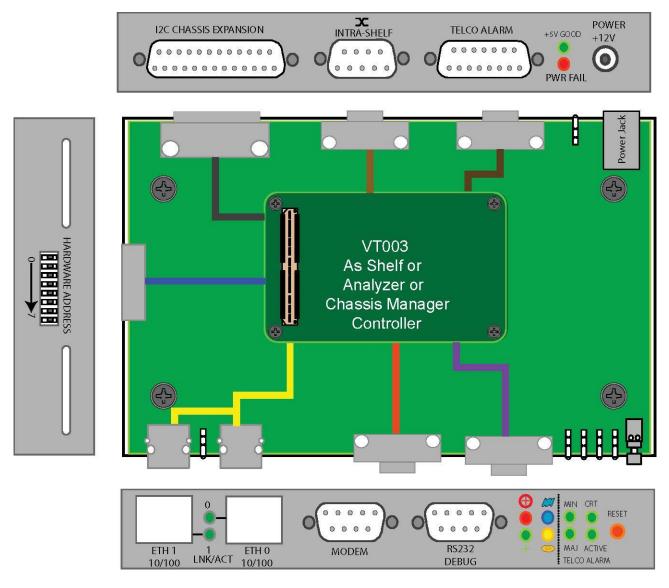


Figure 3: VT007 Functional Block Diagram

IPMI Protocol Analyzer

VT007 can be used as an IPMI protocol analyzer. Figure 4 shows the trace viewer output.

- -	latform Event &&Request				▼.	Express	ion Apply		
о.	Time E	Bus	Dir	Src	Dest	Seq	Net Fn	Command	
22		IPMB-A	REQ	0x92	0x20	16	Sensor/Event	Platform Event	
24		IPMB-A	REQ	0x88	0x20	1	Sensor/Event	Platform Event	
25		IPMB-A	REQ	0×90	0x20	20	Sensor/Event	Platform Event	
28		IPMB-B	REQ	0×88	0x20	2	Sensor/Event	Platform Event	
29		IPMB-B	REQ	0×92	0x20	20	Sensor/Event	Platform Event	
30 31		IPMB-A	REQ	0x92	0x20	8 12	Sensor/Event	Platform Event	
32		IPMB-B IPMB-A	REQ REO	0x92 0x92	0×20 0×20	12	Sensor/Event Sensor/Event	Platform Event Platform Event	
35		IPMB-A	REO	0x88	0x20	3	Sensor/Event	Platform Event	
6		IPMB-B	REO	0x90	0x20	20	Sensor/Event	Platform Event	
8		IPMB-B	REO	0x88	0x20	4	Sensor/Event	Platform Event	
19		IPMB-B	REO	0x92	0x20	20	Sensor/Event	Platform Event	
10		IPMB-A	REO	0x92	0x20	8	Sensor/Event	Platform Event	
ŧ1	78.660.000 I	IPMB-B	REQ	0x92	0x20	12	Sensor/Event	Platform Event	
12	78.690.000 I	IPMB-A	REQ	0x92	0x20	16	Sensor/Event	Platform Event	
13	79.020.000 I	IPMB-A	REQ	0x88	0x20	5	Sensor/Event	Platform Event	
		AT TOTAL PR							
		IPMB-A	REQ	0×90	0x20	20	Sensor/Event	Platform Event	
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14 15 16	79.050.000 I 79.430.000 I	IPMB-A IPMB-B IPMR-R	REQ REQ RFO	0x88 0x92	0x20 0x20	6 20	Sensor/Event	Platform Event	
14 15 16	79.050.000 I 79.430.000 I 79.460.000 I equest: 0x88 -> 0x20	IPMB-A IPMB-B IPMR-R	REQ REQ RFO	0x88 0x92	0x20 0x20	6 20	Sensor/Event Sensor/Event	Platform Event	<u> </u>
4 5 6 1 Re	79.050.000 I 79.430.000 I 79.460.000 I equest: 0x88 -> 0x20 Header	IPMB-A IPMB-B IPMR-R O Plat	REQ REQ RFO	0x88 0x92	0x20 0x20 (Sens	6 20	Sensor/Event Sensor/Event	Platform Event	
4 5 6] Re	79.050.000 I 79.460.000 I 79.460.000 I equest: 0x68 -> 0x20 Header Body - Event Message Ra	IPMB-A IPMB-B IPMR-R O Plat	REQ REQ RFO form 1	0x88 0x92 Event	0x20 0x20 (Sens 4)	6 2N or/Eve	Sensor/Event Sensor/Event	Platform Event	<u> </u>
4 5 6] Re	79.050.000 I 79.460.000 I 79.460.000 I equest: 0x68 -> 0x20 Header Body Event Message Ra Sensor Type	IPMB-A IPMB-B IPMR-R O Plat	REQ REQ RFO form 1 : 0 : 0	0x88 0x92 Event 0x04 (0x01 (0x20 0x20 (Sens 4) Tempera	6 2N or/Eve	Sensor/Event Sensor/Event	Platform Event	
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Figure 4: IPMI Protocol Analyzer Trace Viewer Output

Specifications

Architecture				
Physical	Dimensions	Width: 5.685" (144 mm)		
		Depth 7.0" (177.8 mm)		
Туре	Shelf Manager	Stand-Alone (Bench-top)		
Standards				
Module Management	IPMI	IPMI v2.0 and PICMG 3.0		
Configuration				
Power	VT007	3W		
Environmental	Temperature	See Ordering Options		
		Storage Temperature: -40° to +90°C		
	Vibration	Operating 9.8 m/s ² (1G), 5 to 500 Hz on each axis		
	Shock	Operating 30G each axis		
	Relative Humidity	5 to 95% non-condensing		
Front	Interface Connectors	Debug Port, RS-232		
		Dual 10/100 Ethernet RJ-45		
		Modem Interface DB25		
	Push Button			
	LEDs	IPMI management control		
		Activity/Link user LEDs		
Rear	Interface Connectors			
		DB15 with x5 I ² C busses		
		Input power 12V		
Software Support	Operating System	Linux v2.6.15		
Other				
MTBF	MIL Hand book 217-F@ T			
Certifications	Designed to meet FCC, CE and UL certifications, where applicable			
Standards	VadaTech is certified to both the ISO9001:2015 and AS9100D standards			
Warranty	Two (2) years, see <u>VadaTe</u>	ech Terms and Conditions		

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of OpenVPX, ATCA and MTCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTMs), Power Modules, and more. The company also offers integration services as well as preconfigured Application-Ready Platforms. Please contact VadaTech Sales for more information.

5

Ordering Options

VT007 - A00-000-0HJ

A = Software Option	
1 = Shelf Manager 2 = IPMI Protocol Analyzer 3 = Chassis Manager (VPX)	
	H = Temperature Range
	1 = Commercial (Operating Temperature 0° to 65°C) 2 = Industrial
	J = Conformal Coating
	0 = No coating 1 = Humiseal 1A33 Polyurethane 2 = Humiseal 1B31 Acrylic

Related Products

VT003



- Quad Core ARM Freescale processor @ 1 GHz per core
- One GB DDR3 memory
- FRAM for log messages

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VTX980



VTX981



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• One slot benchtop 3U VPX development platform

One slot benchtop 3U VPX development platform

P0 to P2 connectors are installed

• Variable fan speed control for front and rear

- P2 with two VITA 66.5 or 66.4 connectors option
- Support for Rear Transition Modules (RTMs)

Contact

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- Agile production
- · Accelerated deployment
- AS9100 accredited



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